

Electronic Acknowledgement Receipt

EFS ID:	6999926
Application Number:	10807648
International Application Number:	
Confirmation Number:	3901
Title of Invention:	CACHED MEMORY SYSTEM AND CACHE CONTROLLER FOR EMBEDDED DIGITAL SIGNAL PROCESSOR
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Filer Authorized By:	Peter M. Kamarchik
Attorney Docket Number:	030459
Receipt Date:	12-FEB-2010
Filing Date:	24-MAR-2004
Time Stamp:	13:19:48
Application Type:	Utility under 35 USC 111(a)

Payment information:

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Payment Type	Deposit Account
Payment was successfully received in RAM	\$990
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	030459_RCE_02-12-2010.pdf	38850	no	3
			e608aff566699a8c144b1784d57d8d03aa2e6ab4		
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2	Information Disclosure Statement (IDS) Filed (SB/08)	030459_IDS_02-12-2010.pdf	1216122	no	4
			d5ae79c7468d50e5b850c61d3c19438a6549d6db		
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3	Foreign Reference	KR2001-34125.pdf	136195	no	2
			931a9794dc3d3e0e628e07b6d5b69e9b2c0112		
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4	Foreign Reference	WO98013763.pdf	813083	no	21
			5ce9dd0cb47623be8781b315592d10135ebcc0df		
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5	NPL Documents	COMPUTER_ARCHITECTURE_A_QUANTITATIVE_APPROACH_1996_12_31.pdf	586577	no	12
			3ce048456dbaffd75c94d78138b8989cba157dc4		
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6	NPL Documents	030459WO_2005-11-25_ISR.pdf	3701526	no	5
			374b99e8d900608a47d912e048b3814cfc8c833f		
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7	NPL Documents	030459WO_2005-11-25_WO.pdf	4844490	no	9
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8	Foreign Reference	KIPO1999045059.pdf	196269 bcc747d11308b91e6aeb71951cb129eac7d8bf51	no	1
Warnings:					
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9	Fee Worksheet (PTO-875)	fee-info.pdf	32257 8d5ab741ee5afd86f0c2054283a367b2ffb4f752	no	2
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